

**PATENT NUMBER**

## U.S. UTILITY Patent Application

**LATENT DATE**

APPLICATION NO 09/917573	CONT/PRIOR	CLASS 330	SUBCLASS 296	ART UNIT 2817	EXAMINER Shinghtori
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APPLICANTS  
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TITLE  
Active element bias circuit for RF power transistor input

PTO-2040  
12/89

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig. <b>3</b>	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="text-align: center;"> </div>		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____			<b>ISSUE FEE</b>	
	_____ (Primary Examiner) _____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Local Instrument Examiner) _____ (Date)		<b>ISSUE BATCH NUMBER</b>	

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